FIGURE 1

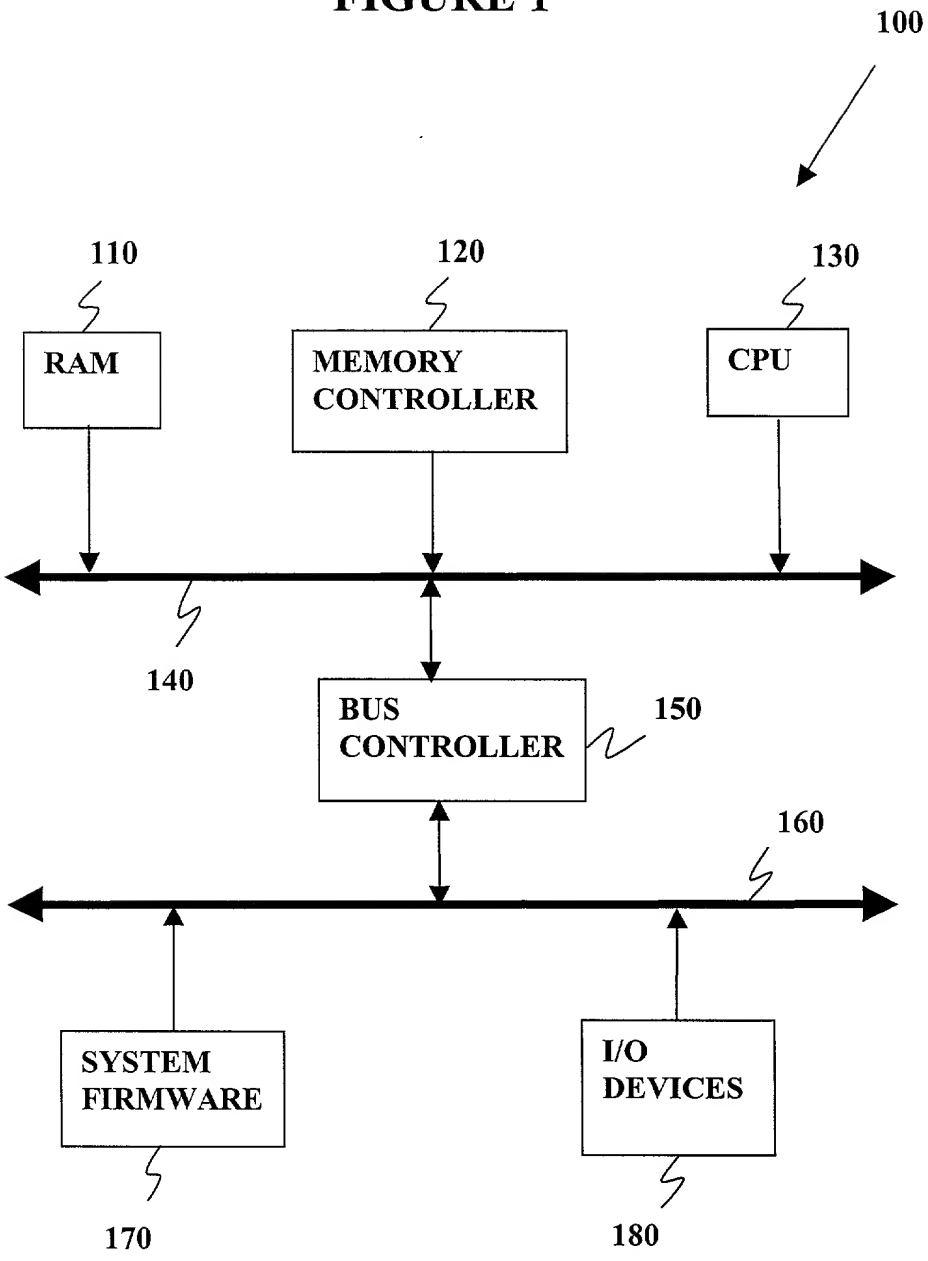
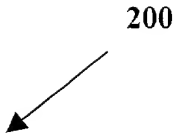

100
110
RAM
120
MEMORY CONTROLLER
130
CPU
140
BUS CONTROLLER
150
160
SYSTEM FIRMWARE
I/O DEVICES
170
180

FIGURE 2



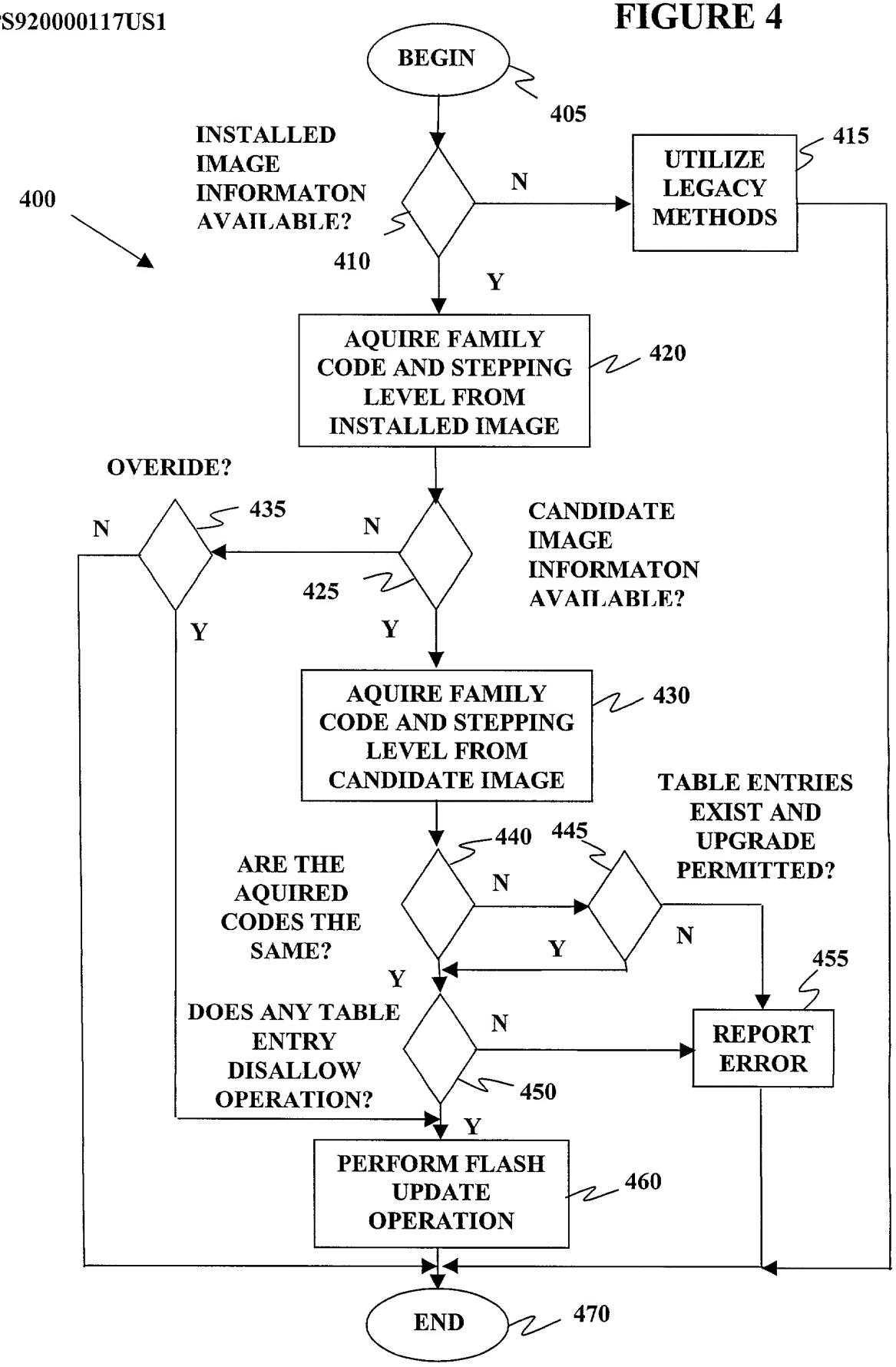
OFFSET	NAME	LENGTH	VALUE	DESCRIPTION	
00h	Identifier	4 bytes	'FFCB'	Character string marks start of control block	210
04h	Present Firmware Family Code	1 word	Varies	Architecturally assigned family code which this image belongs to	220
06h	Present Firmware Stepping Level	1 byte	Varies	Stepping level automatically incremented by one for each new build	230
07h	First table entry	4 bytes	Varies	First compatibility table entry (optional)	240
0Bh	Second table entry	4 bytes	Varies	Second compatibility table entry (optional)	250
. .	. .	. .	. .	. .	
03h + 4*N	Nth table entry	4 bytes	Varies	Nth compatibility table entry (optional)	260
07h + 4*N	End of table marker	4 bytes	00h 00h 00h 00h	Table entry of all 0's marks end of table	270

300

FIGURE 3

OFFSET	NAME	LENGTH	VALUE	DESCRIPTION
00h	Relationship Code	1 byte	Varies	Code describing relationship between the present image and other images which may be compared to it 310
01h	Comparison Firmware Family Code	1 word	Varies (0001h or larger)	Firmware family code of another image which may be compared to the present image 320
03h	Comparison Firmware Stepping Level	1 byte	Varies (01h or larger)	Firmware stepping level of another image which may be compatible to the present image 330

FIGURE 4


BEGIN
405
400
INSTALLED IMAGE INFORMATON AVAILABLE?
410
N
UTILIZE LEGACY METHODS
415
Y
AQUIRE FAMILY CODE AND STEPPING LEVEL FROM INSTALLED IMAGE
420
OVERIDE?
435
N
N
CANDIDATE IMAGE INFORMATON AVAILABLE?
425
Y
Y
AQUIRE FAMILY CODE AND STEPPING LEVEL FROM CANDIDATE IMAGE
430
TABLE ENTRIES EXIST AND UPGRADE PERMITTED?
440
445
ARE THE AQUIRED CODES THE SAME?
N
N
Y
Y
455
DOES ANY TABLE ENTRY DISALLOW OPERATION?
N
REPORT ERROR
450
Y
PERFORM FLASH UPDATE OPERATION
460
END
470